



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No. 9216

Arthur Ray Alexander

Group Art Unit: 2827

Serial No.: 09/752,352

Examiner: I.B. Patel

Filed: 12/29/2000

For: INTRODUCING LOSS IN A POWER BUS TO REDUCE EMI AND
ELECTRICAL NOISE

Assistant Commissioner for Patents
Washington, D.C. 20231

AMENDMENT

This is a reply to the office action dated August 14, 2002, in this application.
Claims 1-3, 5-8, 17, 19 and 20 are pending. Please amend claim 20 as follows.

20. (Amended) A printed circuit board comprising multiple layers, including:
at least one layer on which electronic circuitry resides;
at least one power layer for use in providing electric current to the electronic
circuitry, comprising:

at least one resistive element formed in a void in the power layer and connected
electrically to the power layer to suppress electrical noise created by sudden changes in
current flow; and

01/29/2003 JMCNILLA 00000005 ~~and one capacitive element connected in series with the resistive element.~~

01 FC:1251 110.00 CH

REMARKS

Claims 1-3, 5-8, 17, 19 and 20 are pending in this application. The Office has
rejected claim 20 under 35 U.S.C. § 112 as being indefinite. Claims 1, 2, 6, 17 and 20
were rejected under 35 U.S.C. § 102(e) in view of Archambeault. Claims 7, 8 and 19
were rejected under 35 U.S.C. § 103(a) in view of Archambeault and Klaser, and claims
3 and 5 were rejected in view of Archambeault, Ehman and Nakanishi.

TECHNICAL
RECEIVED
NOV 20 2002
U.S. PATENT AND TRADEMARK OFFICE